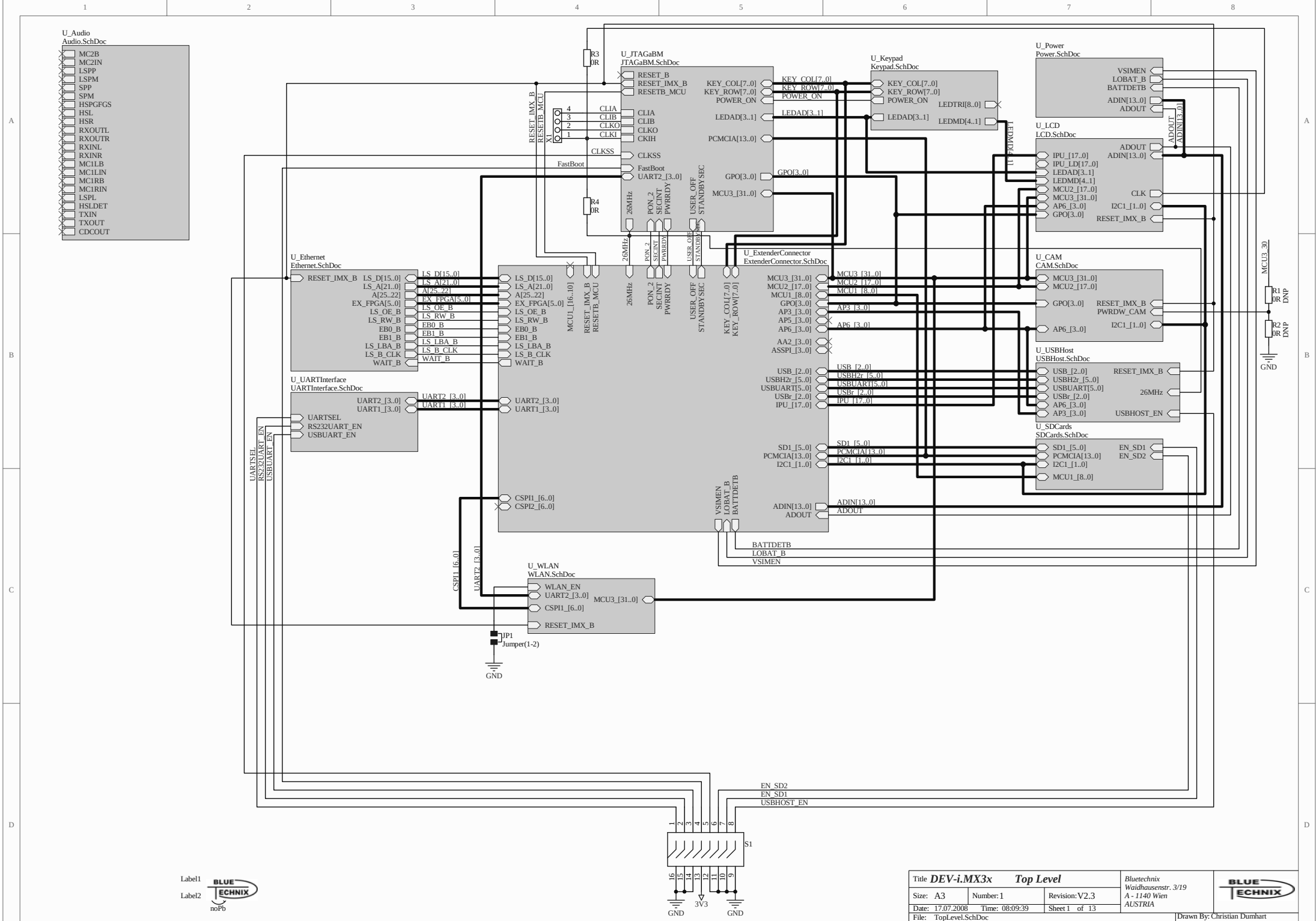
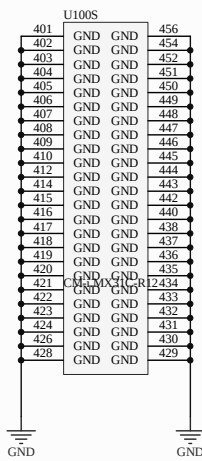
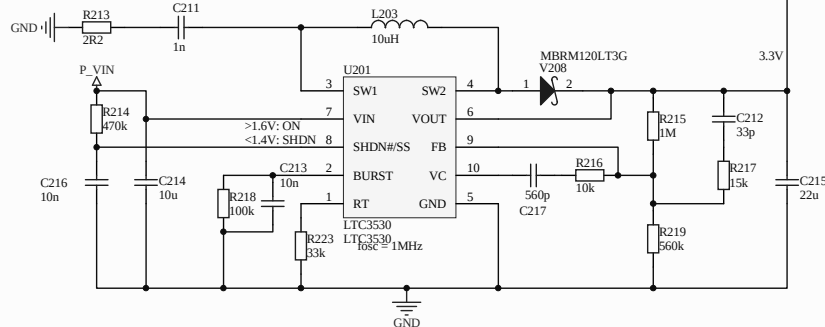
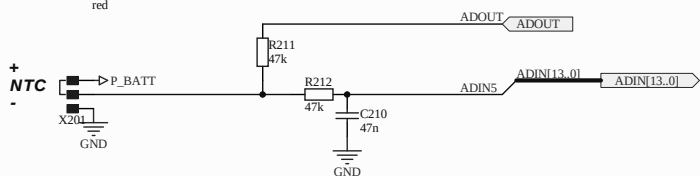
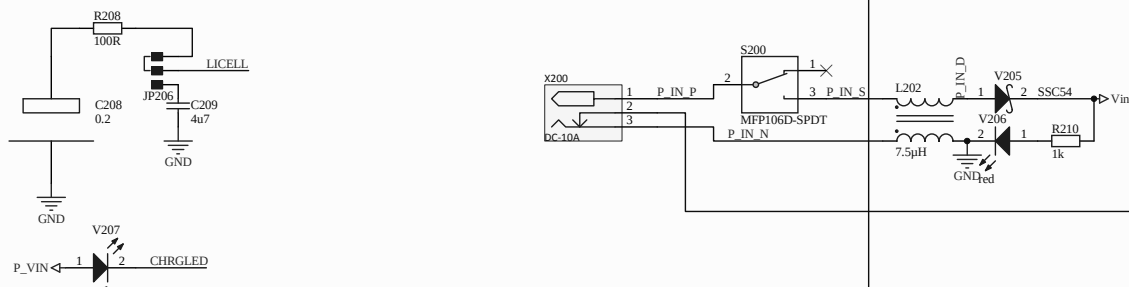
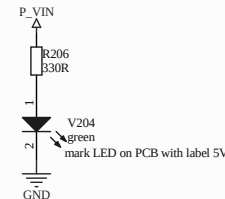
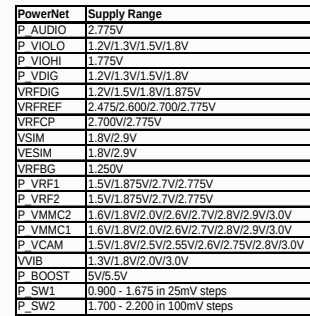
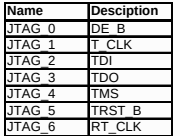
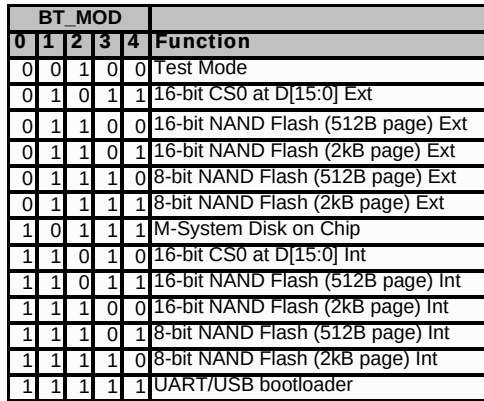


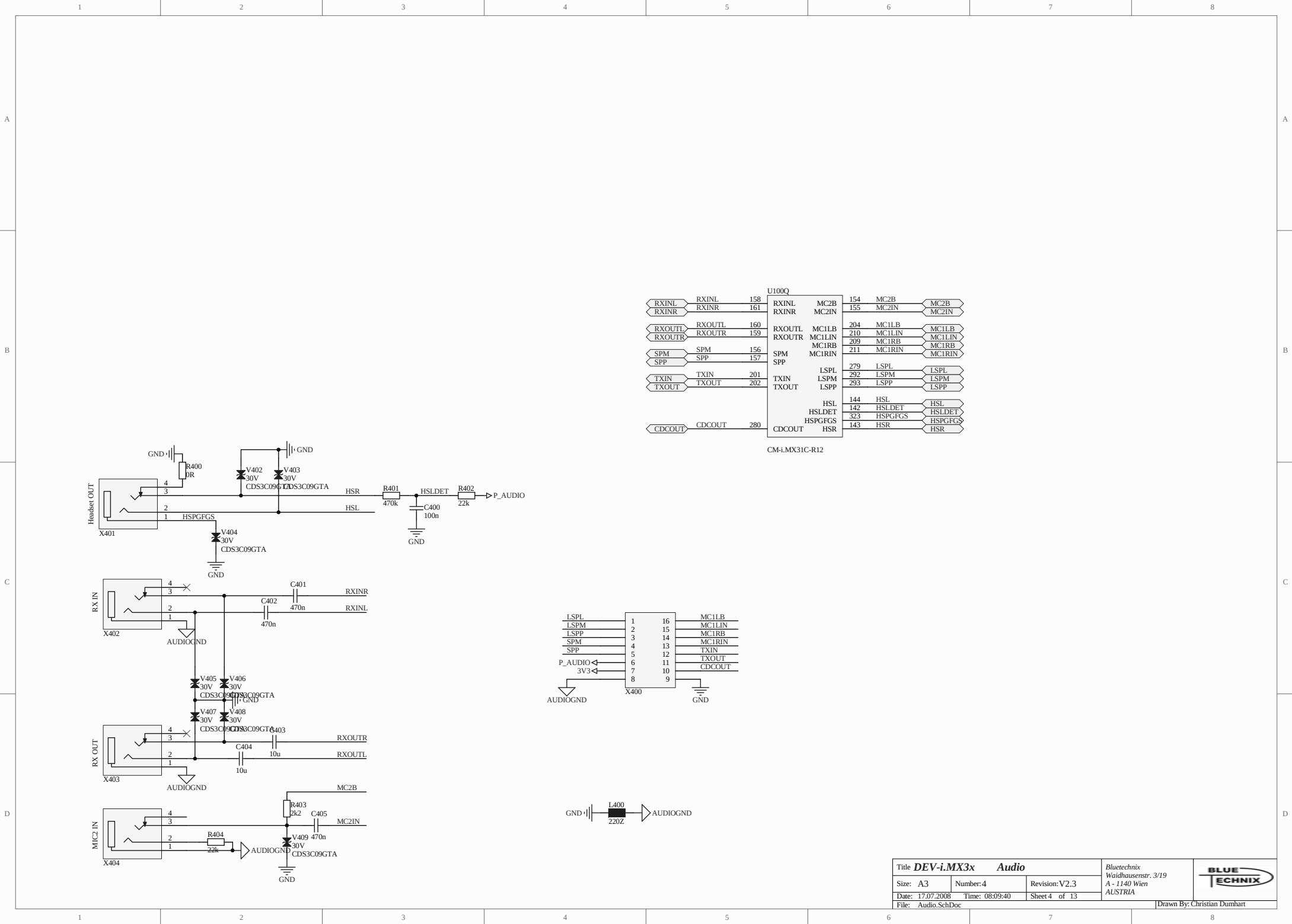


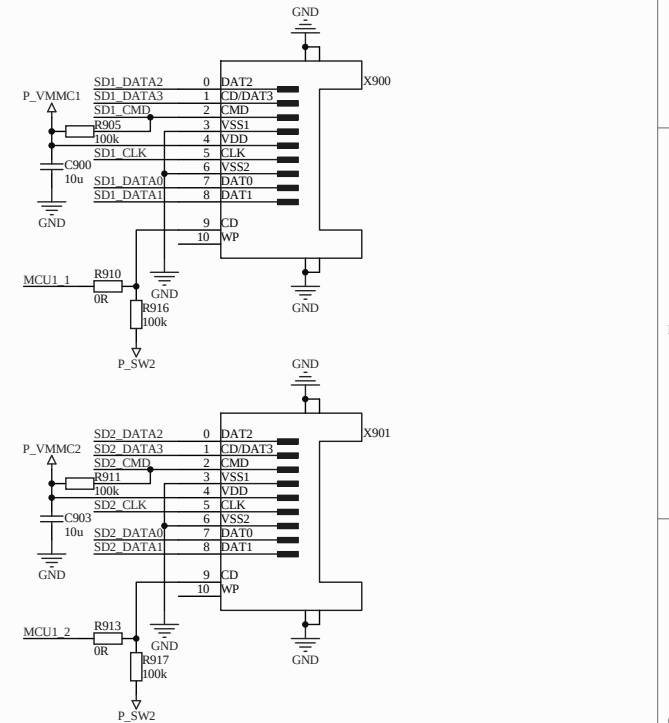
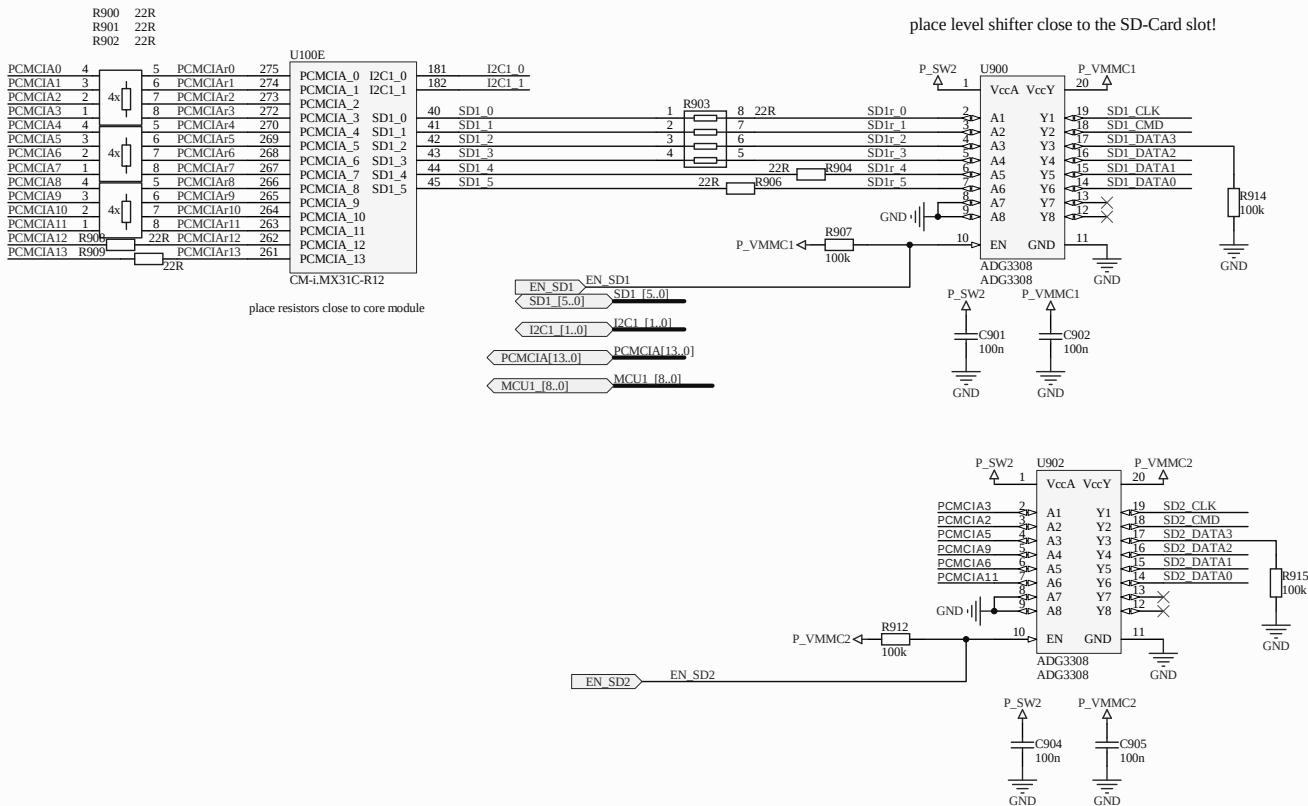




The schematic diagram illustrates the connection between the USBOT Atlas module and the CMiMX21C R12 module. The USBOT Atlas module is shown at the top, featuring a USB connector with pins 1 through 10. The module includes several components: L300 (60Z), C303 (4u7), L301 (60Z), R317 (60Z), R318 (60Z), R319 (60Z), R320 (100k), R321 (0R), S305 (switch), and a POWER_ON button. The module is connected to the CMiMX21C R12 module via a ribbon cable. The CMiMX21C R12 module is shown at the bottom, with various pins and their connections. The connections include: POWER_ON to POWER_ON, P_ATLAS to P_ATLAS, P_BOOST to P_BOOST, and various data and control lines. The USBOT Atlas module is connected to the CMiMX21C R12 module via a ribbon cable. The connections include: POWER_ON to POWER_ON, P_ATLAS to P_ATLAS, P_BOOST to P_BOOST, and various data and control lines. The USBOT Atlas module is connected to the CMiMX21C R12 module via a ribbon cable. The connections include: POWER_ON to POWER_ON, P_ATLAS to P_ATLAS, P_BOOST to P_BOOST, and various data and control lines.

Title DEV-i.MX3x JTAG			
Size: A3	Number: 3	Revision: V2.3	
Date: 17.07.2008	Time: 08:09:40	Sheet 3 of 13	
File: JTAGaBM_SchDoc			
			Drawn By: Christian Dumhart

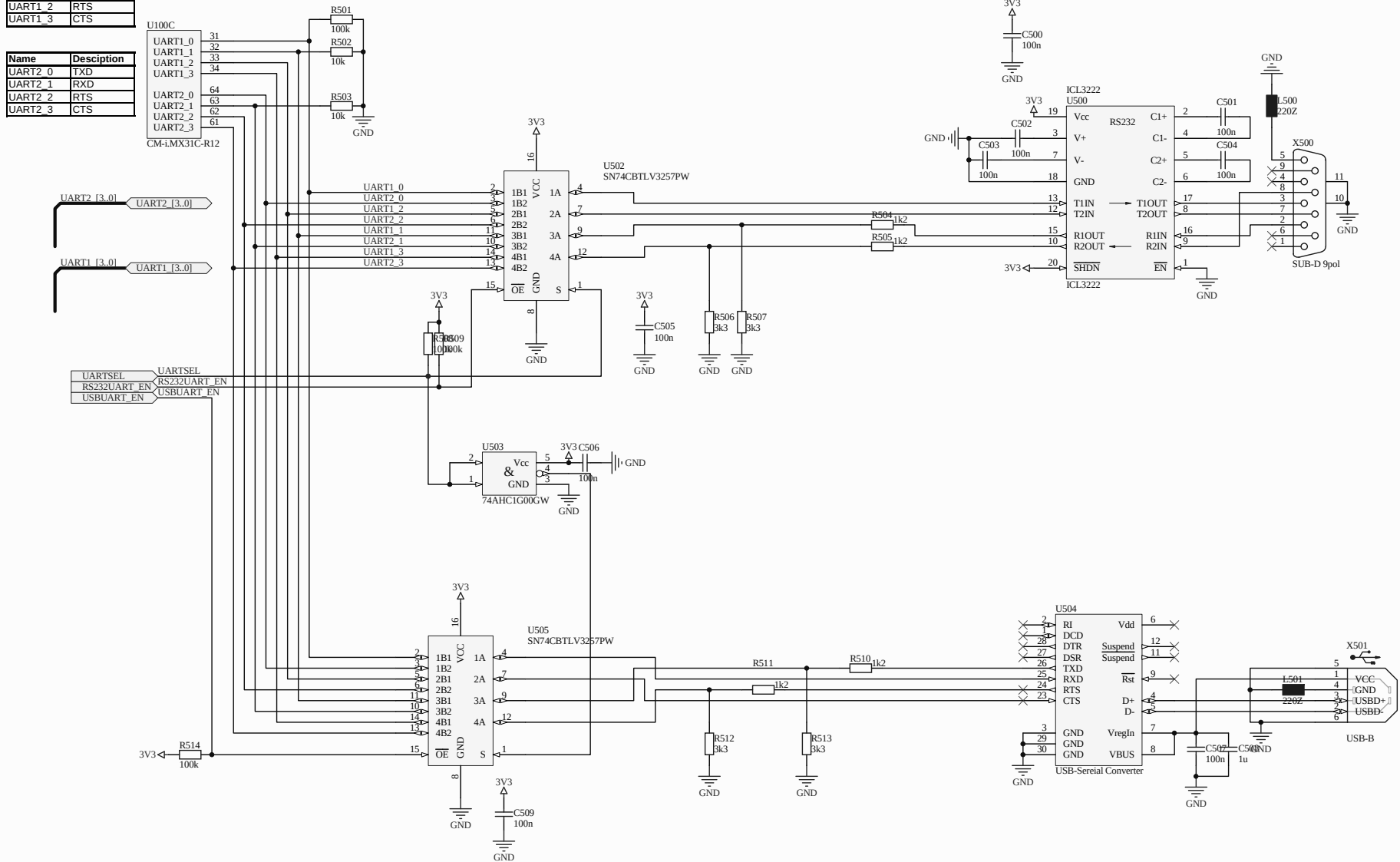


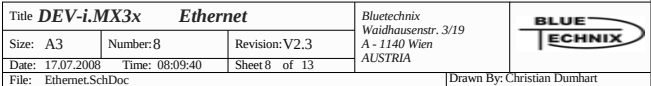


PowerNet	Supply Range
P_AUDIO	2.775V
P_VIOLO	1.2V/1.3V/1.5V/1.8V
P_VIOHII	1.775V
P_VDIG	1.2V/1.3V/1.5V/1.8V
VRFDIG	1.2V/1.5V/1.8V/1.875V
VRREF	2.475/2.600/2.700/2.775V
VRFCP	2.700V/2.775V
VSIM	1.8V/2.9V
VESIM	1.8V/2.9V
VRFBG	1.250V
P_VRF1	1.5V/1.875V/2.7V/2.775V
P_VRF2	1.5V/1.875V/2.7V/2.775V
P_VMMC2	1.6V/1.8V/2.0V/2.6V/2.7V/2.8V/2.9V/3.0V
P_VMMC1	1.6V/1.8V/2.0V/2.6V/2.7V/2.8V/2.9V/3.0V
P_VCAM	1.5V/1.8V/2.5V/2.55V/2.6V/2.75V/2.8V/3.0V
VVIB	1.3V/1.8V/2.0V/3.0V
P_BOOST	5V/5.5V
P_SW1	0.900 - 1.675 in 25mV steps
P_SW2	1.700 - 2.200 in 100mV steps

Name	Description
UART1_0	TXD
UART1_1	RXD
UART1_2	RTS
UART1_3	CTS

Name	Description
UART2_0	TXD
UART2_1	RXD
UART2_2	RTS
UART2_3	CTS





A

B

C

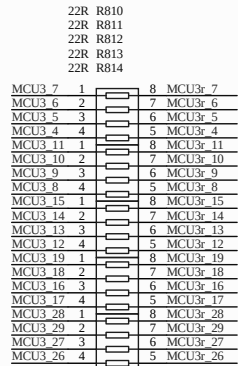
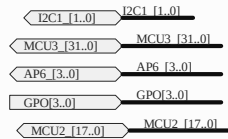
D

A

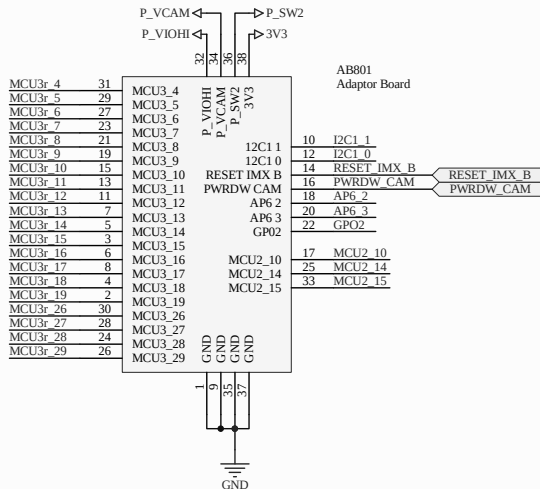
B

C

D

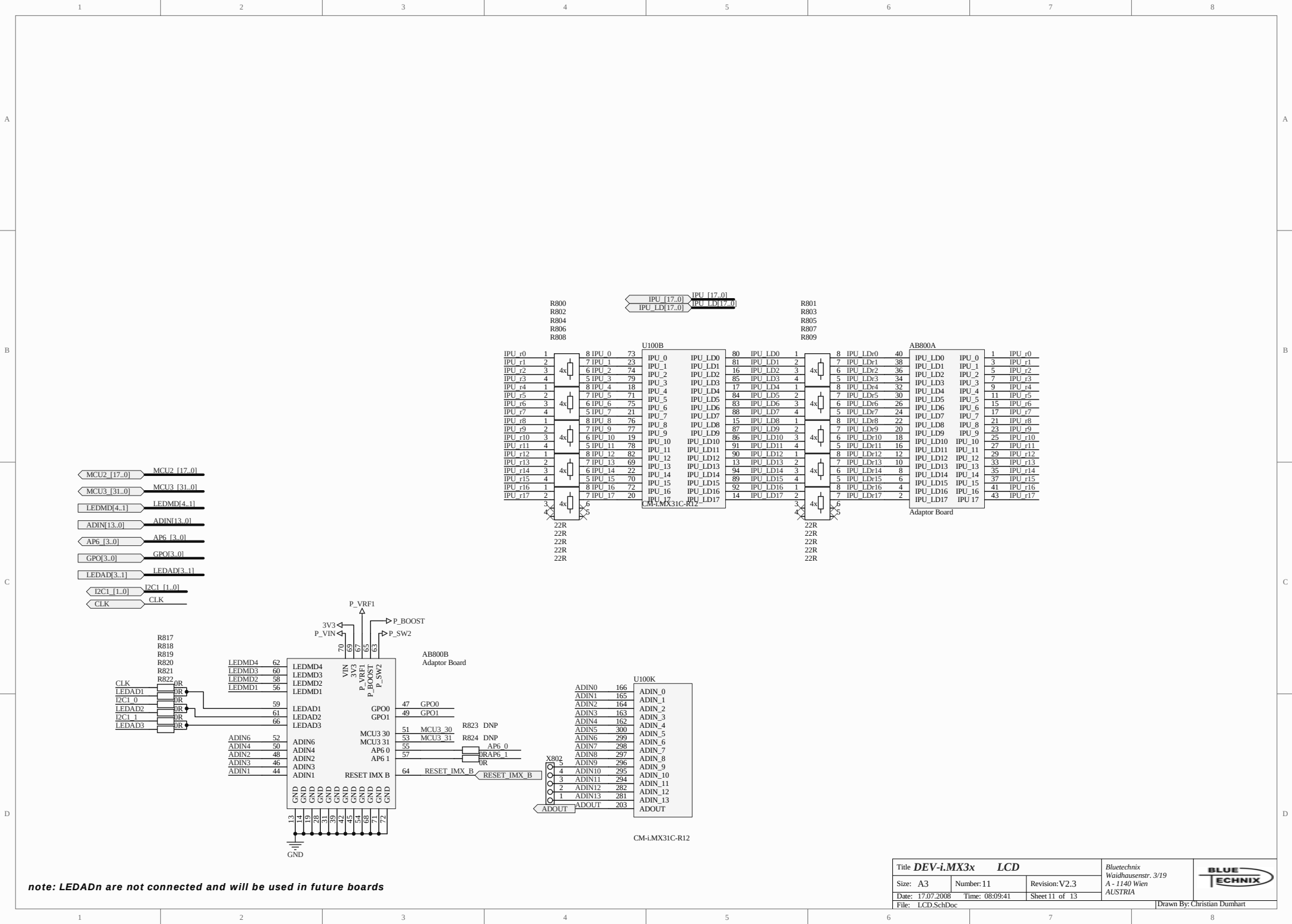


place resistors near adaptor board



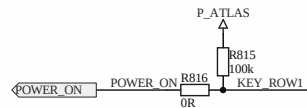
Netname	Function
MCU3_4	CSI_D4
MCU3_5	CSI_D5
MCU3_6	CSI_D6
MCU3_7	CSI_D7
MCU3_8	CSI_D8
MCU3_9	CSI_D9
MCU3_10	CSI_D10
MCU3_11	CSI_D11
MCU3_12	CSI_D12
MCU3_13	CSI_D13
MCU3_14	CSI_D14
MCU3_15	CSI_D15
MCU3_16	CSI_MCLK
MCU3_17	CSI_VSYNC
MCU3_18	CSI_HSYNC
MCU3_19	CSI_PIXCLK
MCU3_26	CSI_D0
MCU3_27	CSI_D1
MCU3_28	CSI_D2
MCU3_29	CSI_D3
MCU3_30	MCU3_30
MCU3_31	MCU3_31



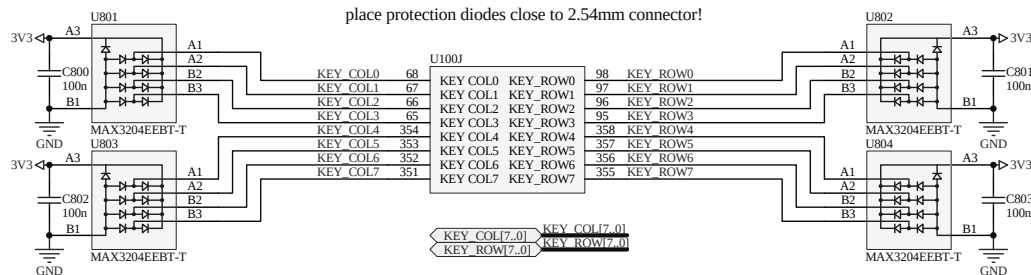


note: LEDADn are not connected and will be used in future boards

Title DEV-i.MX3x LCD			Bluetechnik Waidhausenstr. 3/19 A - 1140 Wien AUSTRIA	
Size: A3	Number: 11	Revision: V2.3		
Date: 17.07.2008	Time: 08:09:41	Sheet 11 of 13		
File: LCD.SchDoc				
Drawn By: Christian Dumhart				



place both connector in 2.54mm raster for external number block



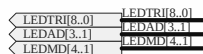
KEY_COL0	1	KEY_COL0	KEY_ROW0	2	KEY_ROW0
KEY_COL1	3	KEY_COL1	KEY_ROW1	4	KEY_ROW1
KEY_COL2	5	KEY_COL2	KEY_ROW2	6	KEY_ROW2
KEY_COL3	7	KEY_COL3	KEY_ROW3	8	KEY_ROW3
KEY_COL4	9	KEY_COL4	KEY_ROW4	10	KEY_ROW4
KEY_COL5	11	KEY_COL5	KEY_ROW5	12	KEY_ROW5
KEY_COL6	13	KEY_COL6	KEY_ROW6	14	KEY_ROW6
KEY_COL7	15	KEY_COL7	KEY_ROW7	16	KEY_ROW7

AB802A Adaptor Board

CM-LMX31C-R12

Keypad

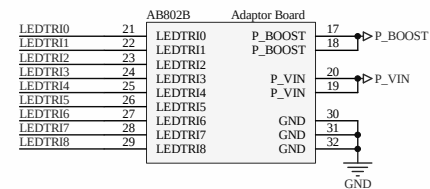
LED Driver

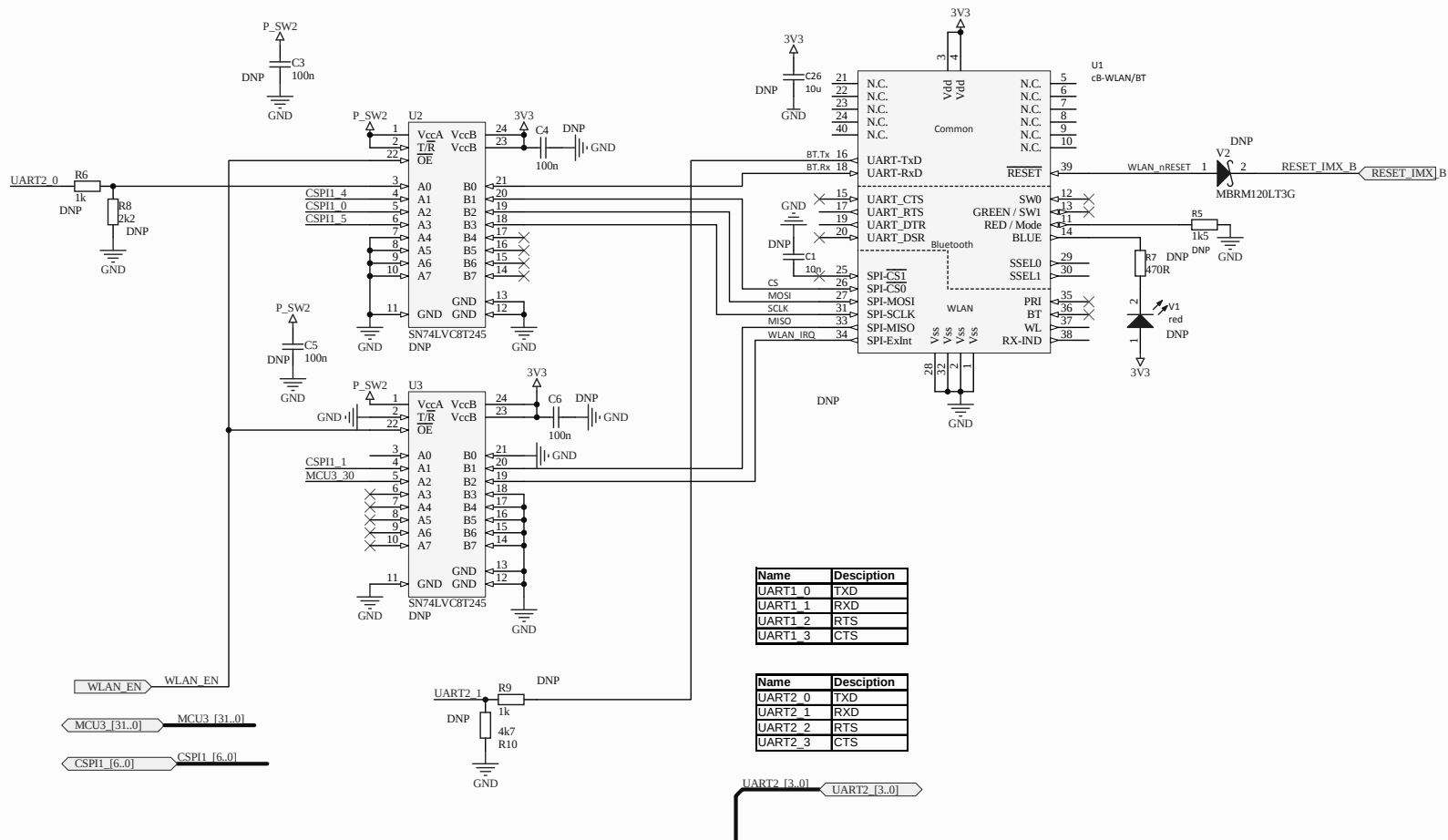


LEDAD1	132	LEDAD_1	LEDTRI_0	320	LEDTRI0
LEDAD2	316	LEDAD_2	LEDTRI_1	321	LEDTRI1
LEDAD3	317	LEDAD_3	LEDTRI_2	322	LEDTRI2
			LEDTRI_3	324	LEDTRI3
LEDMD1	128	LEDMD_1	LEDTRI_4	325	LEDTRI4
LEDMD2	129	LEDMD_2	LEDTRI_5	326	LEDTRI5
LEDMD3	130	LEDMD_3	LEDTRI_6	328	LEDTRI6
LEDMD4	131	LEDMD_4	LEDTRI_7	329	LEDTRI7
			LEDTRI_8	330	LEDTRI8

CM-LMX31C-R12

place protection diodes close to 2.54mm connector!





Name	Description
UART1_0	TXD
UART1_1	RXD
UART1_2	RTS
UART1_3	CTS

Name	Description
UART2_0	TXD
UART2_1	RXD
UART2_2	RTS
UART2_3	CTS